

ABSTRACT OF THE DISCLOSURE

In a signal transmission system between  
a plurality of semiconductor apparatuses, a logic level  
decision circuit deciding a logic level of an input  
5 signal in accordance with which of two reference  
signals a signal level of the input signal is close to,  
by using two reference signals Vref1, Vref0 having  
a "1" level and a "0" level as reference signals for  
deciding the logic level of the input signal having  
10 a binary logic level, is used as an input receiver of  
the each semiconductor apparatus.